## 10/543166 JC17 Rec'd PCT/PTO 2.2 JUL 2005

Docket No.: ISH-0234

(PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Hideki Yamanaka et al.

Application No.: Not Yet Assigned

Confirmation No.: N/A

Filed: Concurrently Herewith

Art Unit: N/A

For: SILICON SEMICONDUCTOR SUBSTRATE

AND METHOD FOR MANUFACTURING

THE SAME

Examiner: Not Yet Assigned

## FIRST PRELIMINARY AMENDMENT

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

## **INTRODUCTORY COMMENTS**

Prior to examination on the merits, please amend the above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 4 of this paper.